FIG. 1

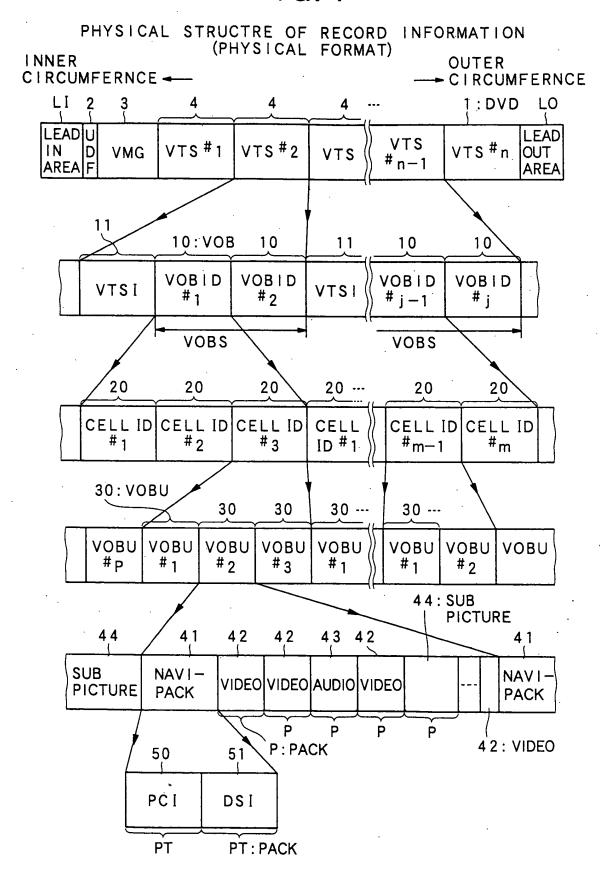


FIG.2

LOGICAL STRUCTRE OF RECORD INFORMATION

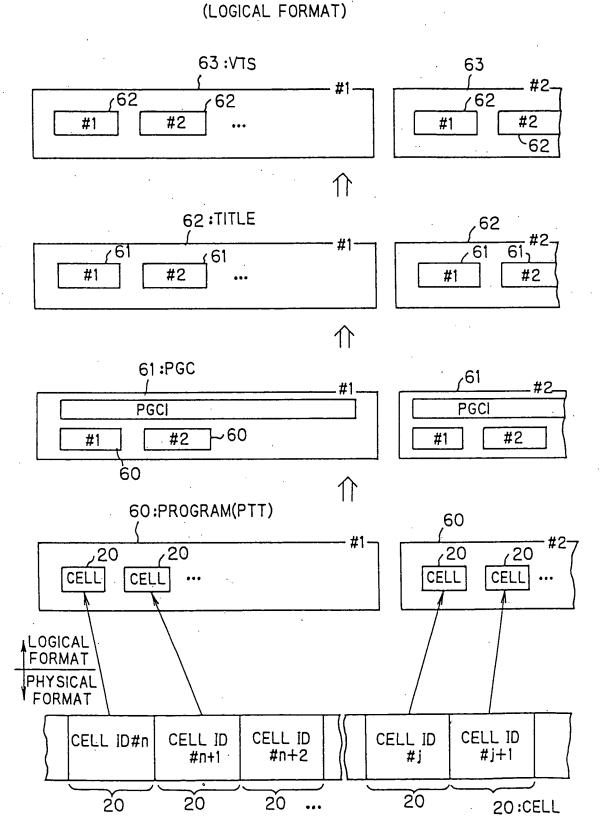


FIG. 3

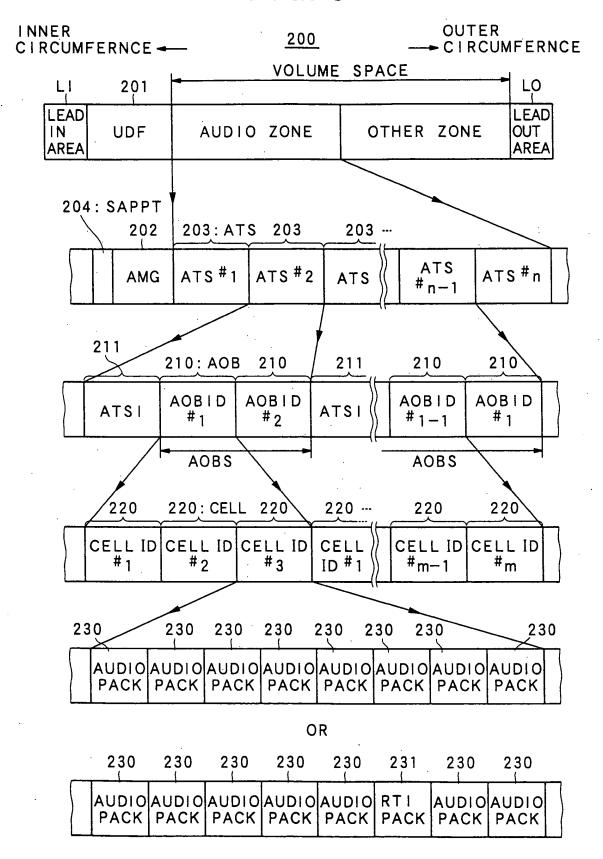


FIG. 4

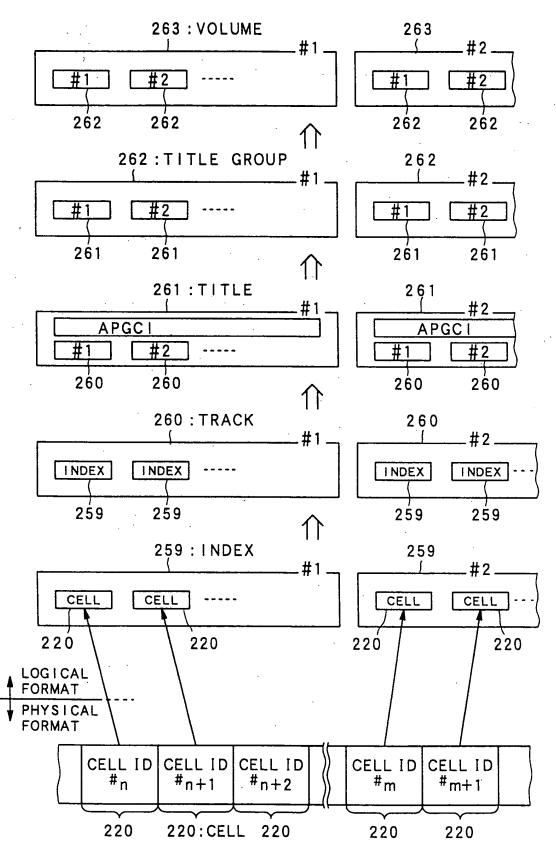
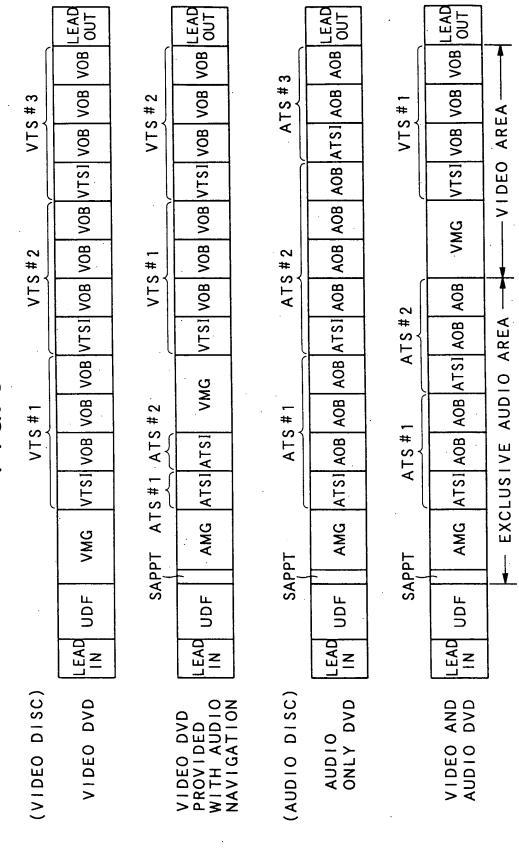
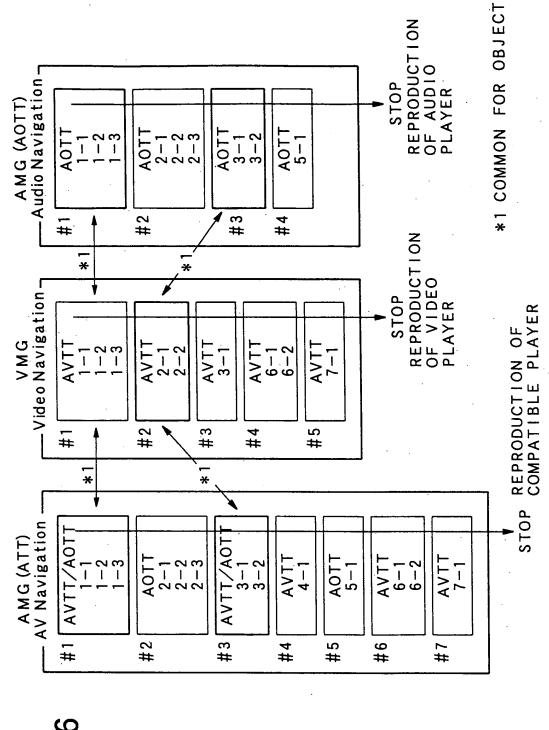
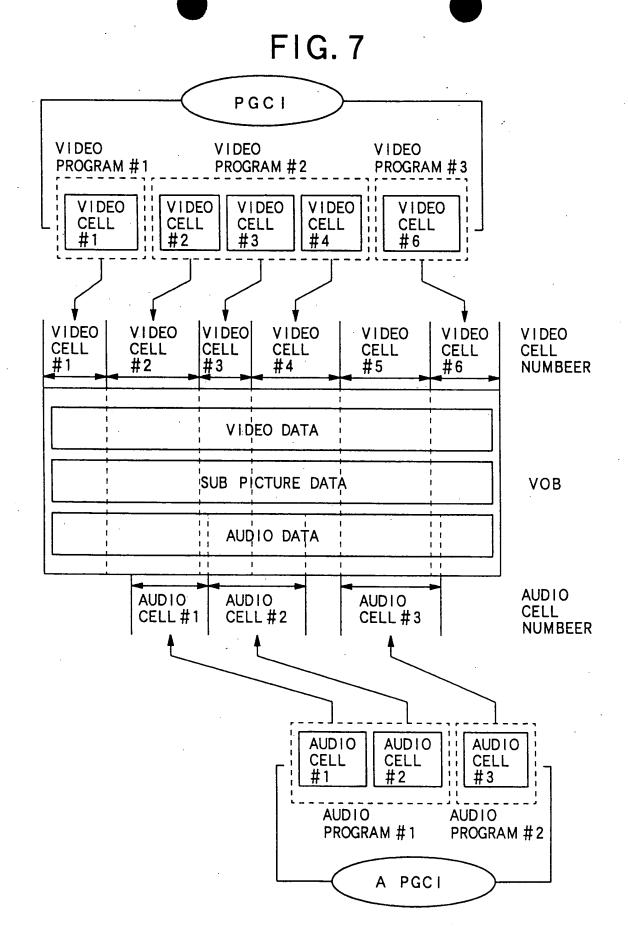


FIG. 5





F1G. 6



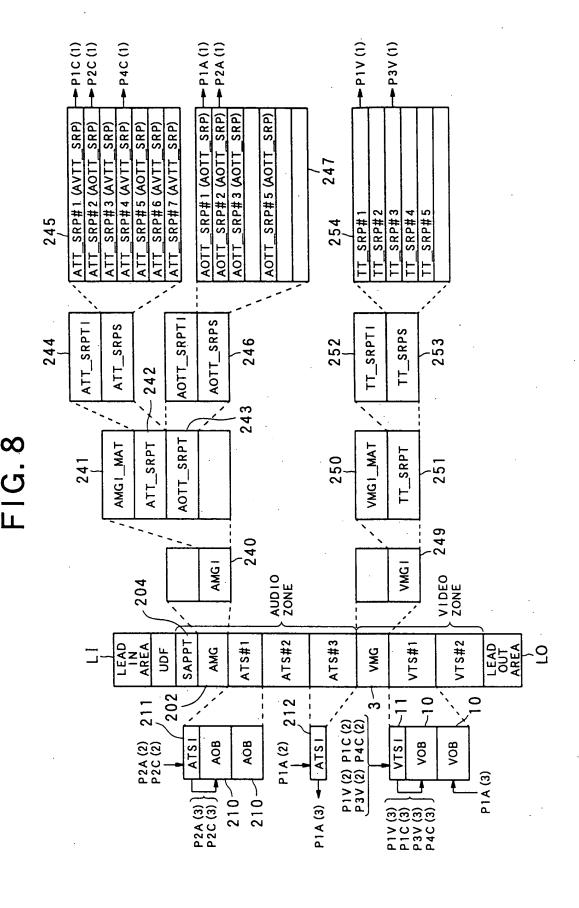


FIG.9A

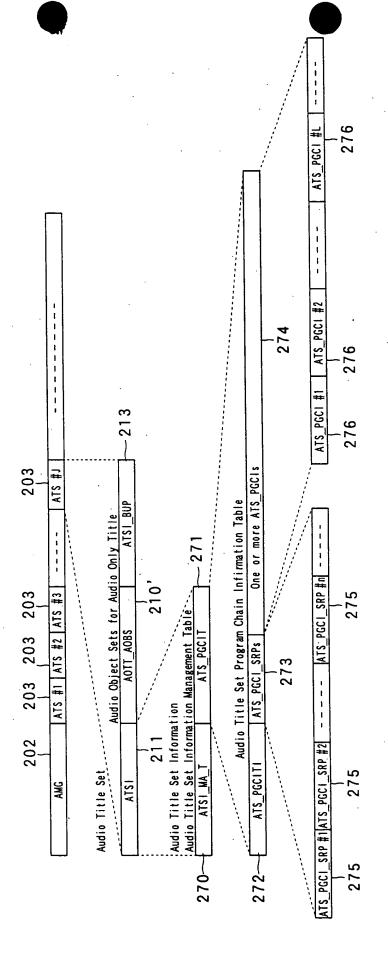


FIG. 9B

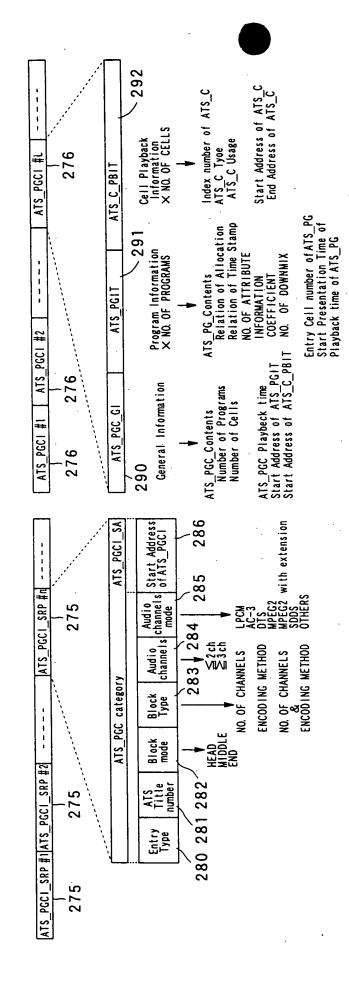
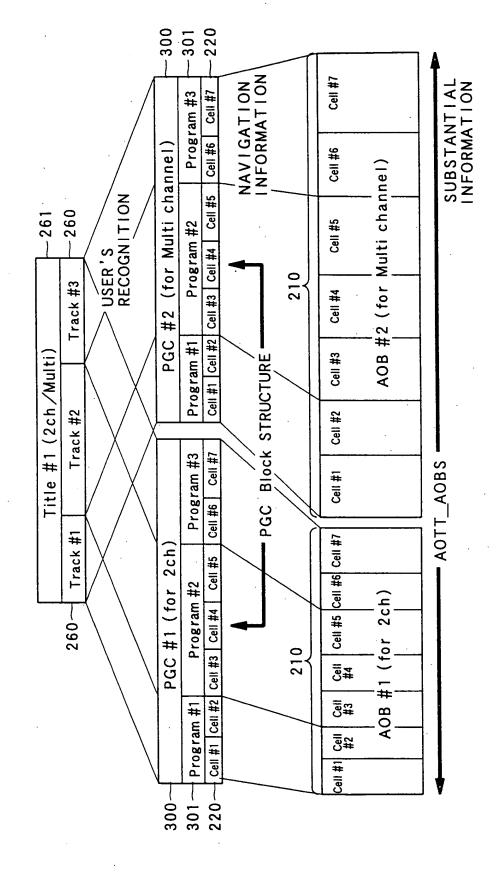


FIG. 10



正 の 二

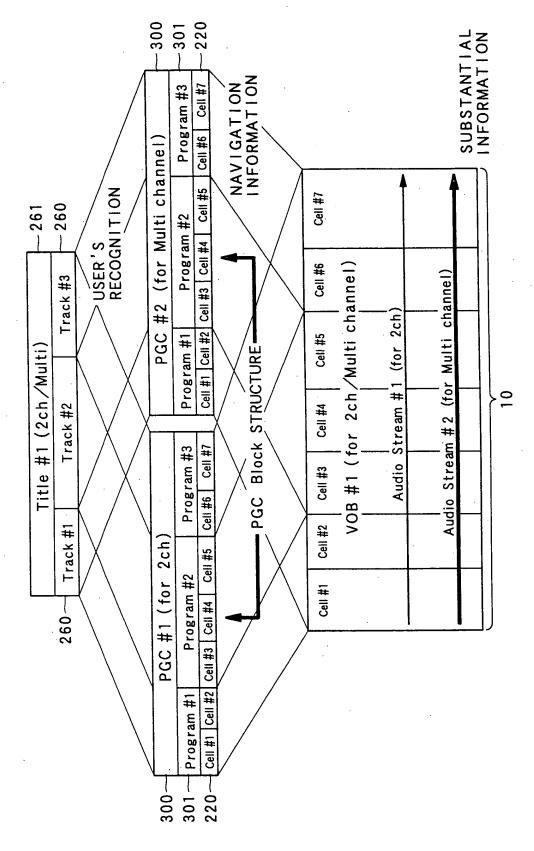
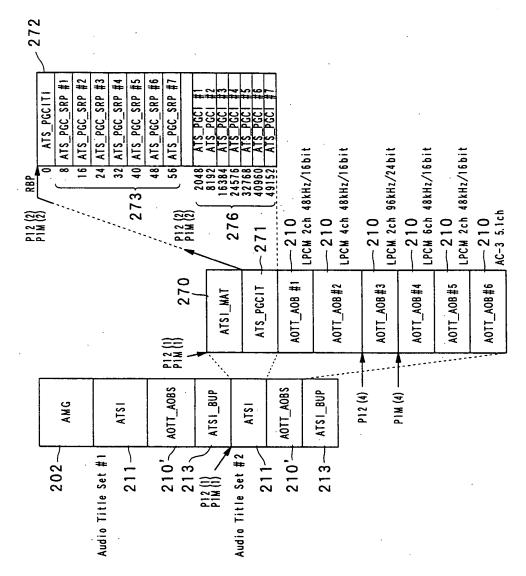


FIG.12A



. . .H

FIG.12B

	P12 (3)	PGC block	P 1M (3)	PGC block									
286	8192	16384 7	32768	40960	49152					•			
285 Audio coding mode	LPCM	LPCM	LPCM	AC-3	LPCM								·
284 Audio channels	23ch ≥3ch	2ch/mono ≥3ch	2ch/mono	≧3ch	2ch/mono								
282 283 / / Block mode Block type	not block	first NO OF CHANNELS last NO OF CHANNELS	first NO. OF CHANNELS & ENCODING METHOD	last NO. OF CHANNELS & ENCODING METHOD	not block	MAIN INFORMATION	TOTAL REPRODUCTION TIME	RELATION BETWEEN ATTRIBUTE AND CELL OF EACH PROGRAM	ADDRESS OF EACH CELL	P12 (4)	TOTAL REPRODUCTION TIME	RELATION BETWEEN ATTRIBUTE AND CELL OF EACH PROGRAM	ADDRESS OF EACH CELL PIM (4)
	SRP #2 O 2		SRP #5 O 4		SRP #7 O 5	===	1 ATS PGC_GI	S 90	ATS_C_PBIT	292	PIM (3) ATS_PGC_GI	290 291	292 ATS_C_PBIT
ATS_P	16 ATS_PGC_SRP	273 32 ATS PGC SRP #3	6	48 ATS_PGC_SRP #6	S6 ATS_PGC_SRP	ATS	16384 ATS PGC 0 7 E 24576 ATS PGC	32768 ATS 40960 ATS 49152 ATS					

FIG.13A

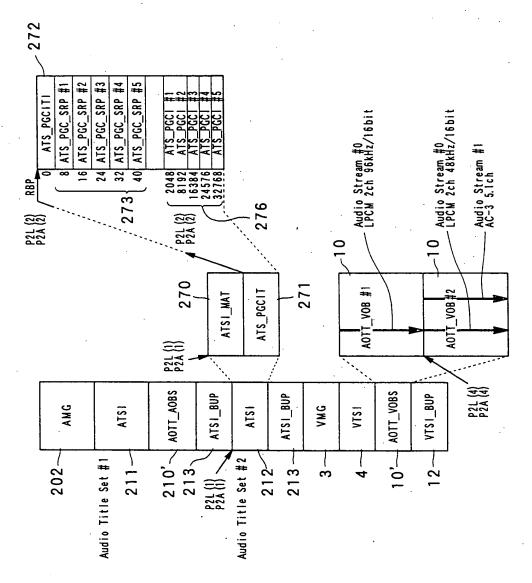
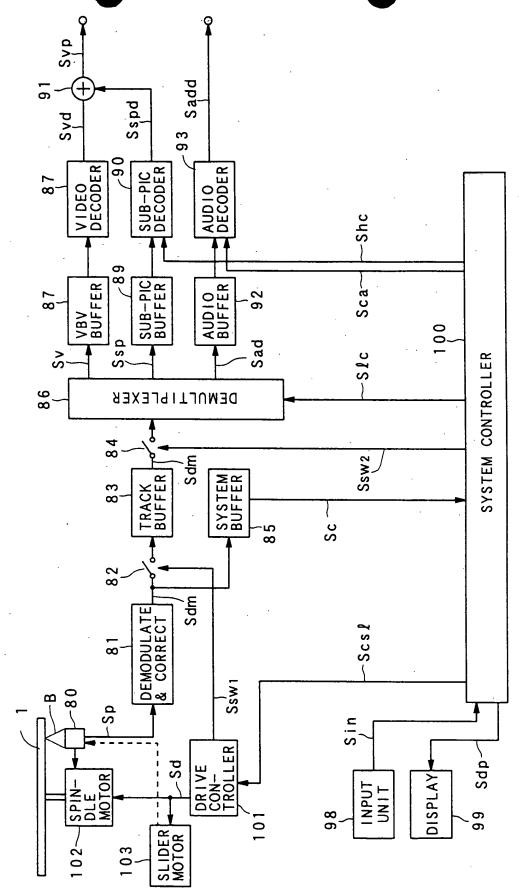


FIG.13B

				P2L (3)	۱	PGC block	P2A (3)					·			
Audio ATS_PGC1 coding ATS_PGC1 mode		2048	8192	16384	24576	32768 >									
Audio coding mode		LPCM	LPCM	LPCM	LPCM	AC-3									
Audio channels		.2ch/mono	2ch/mono	2ch/mono	2ch/mono	≧3ch			٠	NO. = #0				NO = # 1	
					DDING METHOD	DDING METHOD				AUDIO STREAM NO.				AUDIO STREAM NO =	
Block type		not block	not block	not block	NO. OF CHANNELS & ENCODING METHOD	NO. OF CHANNELS & ENCODING METHOD		MAIN INFORMATION	TOTAL REPRODUCTION TIME	RELATION BETWEEN ATTRIBUTE AND CELL OF EACH PROGRAM	ADDRESS OF EACH CELL	► P2L (4)	TOTAL REPRODUCTION TIME	RELATION BETWEEN ATTRIBUTE AND CELL OF EACH PROGRAM	ADDRESS OF EACH CELL
Block mode					first	last		MAIN	TOTAL	RELATI AND CE	ADDRES	22	TOTAL	RELAT! AND CE	ADDRES P2
EXAMPLE IN ATS_PGC_SRP Entry ATS_TTN	~ I		2	33	4	4		290	291 ATS_PGC_GI	ATS_PGIT	ATS_C_PBIT	292	ATS_PGC_GI	ATS_PGIT	ATS_C_PBIT
EXAMP ATS_P Entry	_272	0	0	0	0			P2L (3)	291			ا مدر ال مدرون مدرون		90	2
	0 ATS PGC1TI	8 ATS PGC SRP #1	16 ATS PGC SRP #2	273 24 ATS PGC SRP #3	32	40 ATS_PGC_SRP #5		2048 ATS_ 8192 ATS_	276 16384 ATS PGCI #3 - 24576 ATS PGCI #4 - 32768 ATS PGCI #5 - 3				P2A (3)	29	29

FIG. 14



Sadd 96 97 94 93 RTI DECODER BUFFER AUD 10 DECODER AUDIO DECODER BUFFER RT! DECODER 00a MEMORY Sca AUD 10 BUFFER Shc RT! BUFFER 92 95 100 Sad -SIc SYSTEM CONTROLLER 9 DEMOLTIPLEXER ∞ 84 Ssw₂ Sdm TRACK BUFFER SYSTEM BUFFER 83 Sc 85 82 Sdm DEMODULATE & CORRECT Scsl <u>~</u> Ssw1 -80 Sin 400 DRIVE CON-TROLLER Sdp-PS SPIN-DLE MOTOR DISPLAY HEADPHONE INPUT UNIT 99 98 SLIDER MOTOR 102 -103

FIG. 15

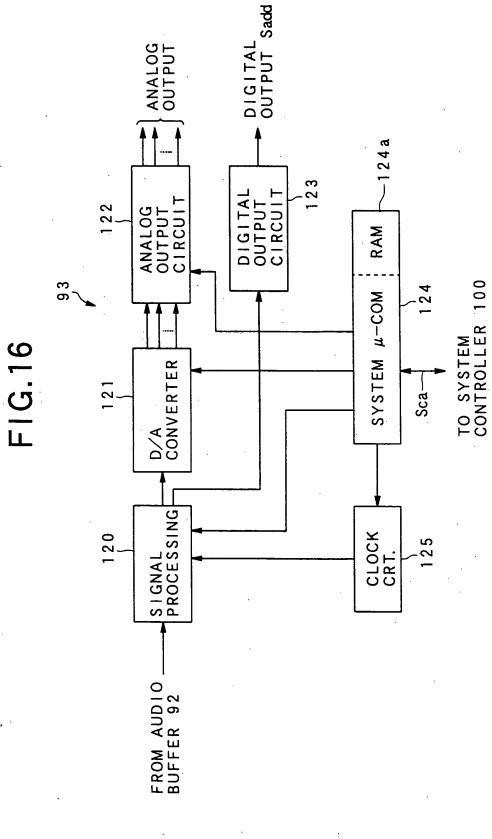
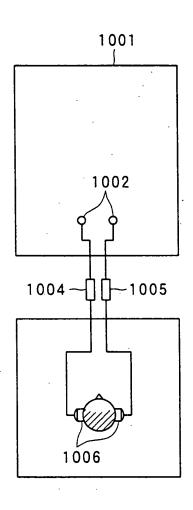


FIG.17A

FIG. 17B



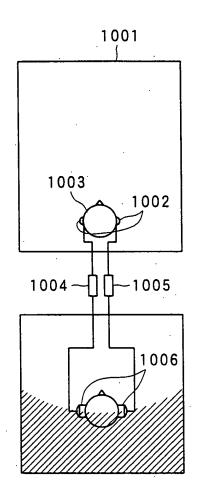


FIG.18

AUDIO PACK 230

#1	#2	#1	#2	#1	#2	#1	
STEREO	BINAÜRAL	STEREO	BINAURAL	STEREO	BINAURAL	STEREO	i

FIG.19

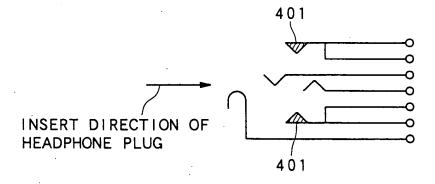


FIG. 20

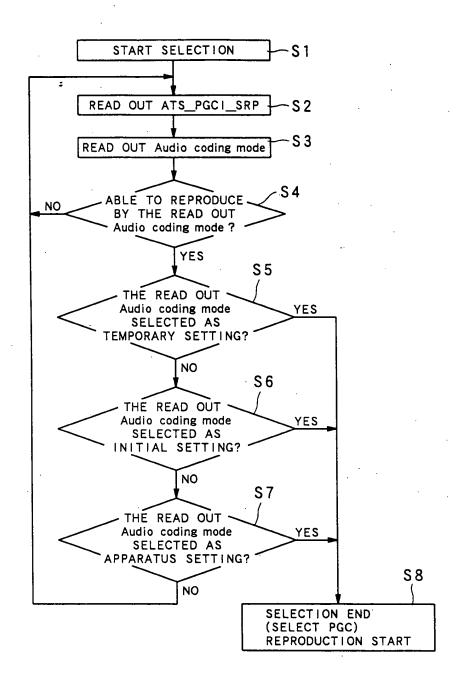


FIG. 21

